Notification
Admissions to PhD. Programmes under Visvesvaraya Scheme for Electronics and IT: Phase-II for the Academic Year 2025-26

(Available in Computer Science and Engineering, Electronics and Communication Engineering, & Electrical Engineering Departments)

NATIONAL INSTITUTE OF TECHNOLOGY WARANGAL

Admissions to PhD. Programmes under Visvesvaraya PhD Scheme for Electronics and IT: Phase-II for the Academic Year 2025-26
(Available in Computer Science and Engineering, Electronics and Communication Engineering, & Electrical Engineering Departments)

NIT Warangal invites applications for admission to the Ph.D. programmes (2025) under the Visvesvaraya Ph.D. Scheme: Phase-II. A total of **five full time fellowships** are available in the Institute under Visvesvaraya Ph.D. scheme out of which two seats are reserved for the Chip to Start-up (C2S) project for Electronics and IT. The vacancies will be distributed in **Department of Computer Science & Engineering (CS), Department of Electronics and Communication Engineering (EC) and Department of Electrical Engineering (EE).** Candidates admitted under Visvesvaraya Ph.D. scheme will be governed by the guidelines issued by the Ministry of Electronics and IT (MEITY), Govt. of India.

1) Salient features of the Visvesvaraya PhD Scheme for Electronics and IT: Phase-II Full-Time PhD Candidates

I. The full-time candidates would be enrolled at the supported institution(NIT Warangal) under the scheme as per the procedural norms and 'Terms & Conditions' of that institution as well as the Terms & Condition of the Visvesvaraya Ph.D. scheme: Phase -II

II. Financial support provided to each Full Time PhD candidate is as mentioned below. Fellowship: Revised to Rs 38,750/- per month (I & II year); Rs 43,750/- (III to V year).

III. Research Contingency Grant Support: An amount of Rs. 1,20,000/Year/Full-time PhD candidate for support duration of PhD candidate as per following guidelines:

    a. Out of Rs. 1.20 Lakh/ year, upto Rs. 30,000/- may be availed by the institute for respective PhD Candidate's miscellaneous expenses relevant to research work- e.g. Books / Documents, Equipment / Software, Consumables / Chemical / Electronic components, Prints of research papers, reports etc., Registration fee for attending Symposia/ Seminars / Conferences in India/abroad where the candidate is presenting an accepted paper, Similar items etc.

    b. Proposal for Rs. 90,000/- or more upto Rs. 1.20 Lakh (the remaining amount after miscellaneous exp.) for lab equipment (excluding civil construction/expansion of the building) should be submitted to PhD Cell, DIC. It should be signed & stamped by the supervisor, nodal & head of the institute.

    c. The amount of Rs. 1.20 Lakh under Research Contingency Grant support would be available to the institute only after completion of tenure of one year for a particular candidate and like-wise on pro rata basis.

    d. The proposal should clearly specify the utility & role of each of the proposed equipment in research of respective PhD candidate(s). The proposal would cover the item wise cost along with total budget and minimum amount required to place purchase order and amount required at the time of delivery of equipment to the institute.

    e. The eligibility/unspent balance of a particular year for each Full-time PhD candidate may be carried forward to the next year, till the candidate is supported under the scheme (5 years/PhD completion etc.).

IV. Reimbursement of Rent for those Full Time Ph.D. candidates who are not provided hostel/institute accommodation: The ceiling for Reimbursement of Rent would be 24%, 16%, and 8% of the fellowship amount as per the Government of India norms for class X, Y or Z cities respectively.

V. One-time support for attending International Conference for selected candidates fulfilling eligibility criteria based on the guidelines to be provided in due course

2) For more details regarding the Visvesvaraya Ph.D. scheme, interested candidates may explore the webpage: http://phd.digitalindiacorporation.in/

3) Minimum qualifications and other academic eligibility requirements for PhD admissions under Visvesvaraya PhD Scheme for Electronics and IT: Phase-II @ NIT Warangal for the academic year 2025-26

Aggregate 60% marks or CGPA of 6.5 (on a 10-point scale) at both UG and PG for GEN/ GEN-EWS/OBC-NCL category and minimum 55% aggregate marks or 6.0 CGPA (on a 10-point scale) for SC/ ST/ PwD category and must have qualified GATE score in the appropriate branch of study.

Master's/M.S. (by Research) degree in Engineering / Technology in the appropriate branch of study and must have qualified GATE score in the appropriate branch of study.

The maximum age for admission into PhD program under Full time Institute Fellowship is 30 years for GEN category, 33 years for OBC-NCL and 35 years for SC/ST/PwD/Female candidates.

DEPARTMENT SPECIFIC QUALIFICATIONS & RESEARCH AREAS:
Department of Computer Science and Engineering (CS)
[https://www.nitw.ac.in/cse]
The candidates applying for admission to Ph.D. Programmes in the Computer Science and Engineering Department must meet the following minimum eligibility requirements:

M.E./M.Tech./M.S. Degree with any one of the degrees (Undergraduate or Postgraduate) in Computer Science and Engineering / Information Technology / Electrical and Electronics Engineering / Electronics and Communication Engineering or other equivalent Engineering disciplines.

Department of Electronics and Communication Engineering (EC)
[https://www.nitw.ac.in/ece]
Passed M.E./M.Tech./M.S. Degree in Electronics and Communication Engineering (ECE)/Electrical Engineering (EE)/Computer Science and Engineering (CSE) or in relevant disciplines related to major areas of research available in ECE Department. Candidates are also required to have passed BE/B. Tech./BS in ECE/EE/CSE or allied discipline.

Candidates selected for Ph.D. under C2S project will be required to carry out research in the broad area of Electronics and Communication Engineering.

Department of Electrical Engineering (EE)
[https://www.nitw.ac.in/eee]
The candidates applying for admission to Ph.D. Programmes in the Electrical Engineering Department must meet the following minimum eligibility requirements:

B.Tech/ B.E. or Equivalent degree in Electrical Engineering or Electrical and Electronics Engineering and M.Tech/ M.E in Electrical Engineering or allied Specializations in broad research areas.

Selection Process:
For selection to Full-time PhD programme under Visvesvaraya PhD Scheme for Electronics and IT: Phase-II, candidates must clear the National level PhD entrance examination conducted by NIT Warangal. Exam will be conducted at NIT Warangal and no TA/DA will be paid for attending the written test/ interview.

4) Important Information:

➢ NIT Warangal is a fully residential Institute. All the admitted Full-time research scholars into NIT Warangal with institute fellowship are required to stay on the campus, and HRA will not be paid.

➢ In case the result of the qualifying degree is awaited, provisional admission is permitted to a candidate subject to meeting the above minimum qualifying degree requirements latest by September 30, 2025. In these cases, all exams should have been completed by August 15, 2025. A certificate from the Head of the current institute to that effect should be submitted during certificate verification.

➢ No working professional shall be admitted as a full-time scholar unless he/she is relieved of his/her duties by his/her present employer and unless a proper reliving and no-objection certificate for pursuing a Ph.D is produced to that effect.

➢ Upon completion of certificate verification, the short-listed candidates for the walk-in interview will be notified on the department notice board, and no separate call letters shall be sent to the candidates.

➢ If any candidate has admitted due to any discrepancies in the processing of the application, the Institute reserves the right to cancel the seat before admission is made final, without payment of any compensation for the inconvenience caused.

➢ If it is found that any of the applicants admitted to the Institute has either furnished wrong information or tampered with the records, his/her admission is liable to be cancelled at any stage.

➢ The Institute reserves the right to cancel the admission of any candidate at any stage (even after admission) if any discrepancy is found (or) on the basis of adverse remarks in the Conduct Certificate.

➢ Wherever ambiguities arise in the interpretation of the Admission Rules, the decision of the Director of this Institute on the matter shall be final.

➢ The Director is authorized to make appropriate decisions with regard to any other details of admission not covered by the rules listed above.

➢ The decision of the Institute in all matters will be final and binding on all the applicants who respond to this admissions notification. No interim correspondence or unsolicited query will be entertained from the applicants in connection with the process of scrutiny/selection, including reasons for not being shortlisted for the written test/interview.

➢ Any legal dispute arising out of this admissions notification is subject to the Courts having jurisdiction over Warangal, Telangana.

5) HOW TO APPLY
Ph.D. Program on Full-Time Basis (with Institute Fellowship):

Candidates must report to the concerned department by 8:30 AM on 07/08/2025. They are required to collect the application form from the department and submit the duly filled-in form physically before the certificate verification process, which begins at 9:00 AM on the same day.

After successful verification of the requisite certificates and fulfillment of eligibility conditions, candidates will be permitted to appear for the interview. Upon successful completion of the interview, as per the Institute's admission process, the list of provisionally selected candidates will be published on the Institute website.

Provisionally selected candidates must join the Ph.D. programme on 08/08/2025 by paying the applicable Institute fee, as per the norms for regular Ph.D. students.

6) Checklist of documents to be uploaded along with the application.

i. SSC/ Class X certificate in support of Date of Birth.
ii. Intermediate/ (10+2)/ Class XII certificate.
iii. Provisional certificate/ Degree certificates and Marks Memos of both UG and PG as applicable.
iv. Category certificate (in case of SC/ ST candidates).
v. For GEN-EWS candidates, EWS certificate issued on or after April 1, 2025 as per the format given in the Annexure-I only is acceptable.
vi. For OBC-NCL candidates, OBC-NCL certificate issued on or after April 1, 2025 as per the format given in the Annexure-II only is acceptable along with the undertaking.
vii. Photograph on application form.
viii. Rank Card of GATE (Mandatory)
ix. Fee payment receipt with transaction number.
x. Statement of Proposal (Research Proposal).
xi. APAAR ID
xii. ADHAAR Card
xiii. Any other documents (List of publications, if any).