CBSE Class 12 Electronic Technology 2011 - Set 114

Question Paper

Conducted by the Central Board of Secondary Education (CBSE)

Time Allowed: 3 Hours **Maximum Marks: 40**

General Instructions

(i) Attempt all questions. Each question carries five marks.

Questions

1.

Give the expression for voltage gain, current gain, input impedance and output impedance for a single stage C.E. amplifier circuit in terms of 'h' parameters.

OR

1.

Draw low frequency hybrid model in C.E. configuration and give physical significance of 'h' parameters.

2.

Explain the working of a direct coupled amplifier. Give its limitation.

OR

2.

Draw typical circuit of a R.C. coupled multistage amplifier and explain its working. Draw its frequency response.

3(a).

Explain the difference between voltage and power amplifier.

3(b).

Define collector efficiency of power amplifier.

OR

3.

Explain the working principle of complementary symmetry push-pull amplifier and give its advantages.

4(a).

Name types of feedback.

4(b).

Derive an expression for the gain of an amplifier using negative feedback.

5.

Explain the working principle of a double tuned amplifier. Draw its frequency response.

6.

Explain Barkhausen criterion for oscillations. Explain the working principle of phase shift oscillator.

7.

Draw a typical circuit of a collector coupled astable multivibrator and explain its working principle.

8.

Explain the importance of ICs in Modern Electronics. Give any four examples of ICs.

- End of Question Paper -