CBSE Class 12 Electronic Technology 2012 - Set 114

Question Paper

Conducted by the Central Board of Secondary Education (CBSE)

Time Allowed: 3 Hours **Maximum Marks: 40**

General Instructions

(i) Attempt all questions. Each question carries five marks.

Questions

1(a).

Define a.c. load line.

1(b).

Draw typical circuit of a single stage CE amplifier with proper biasing components.

OR

1.

Draw low frequency hybrid model in CE configuration and give physical significance of 'h' parameters.

2.

Draw typical circuit of transformer coupled multistage amplifier and explain its working. Draw its frequency response.

OR

2.

Draw frequency response of RC coupled multistage amplifier and explain the physical significance of the terms bandwidth, upper crossover frequency and lower crossover frequency.

3.

Explain the working principle of a push-pull amplifier and give its advantages over single-ended power amplifier.

OR

3(a).

Explain the difference between voltage amplifier and power amplifier.

3(b).

Explain the importance of impedance matching in a power amplifier.

4.

Give physical explanation of the effect of negative feedback on gain stability, distortion and bandwidth of an amplifier.

5.

Explain the working principle of a single tuned amplifier. Draw its frequency response.

6.

Draw the circuit of Wein's bridge oscillator and explain its working principle.

7.

Draw a typical circuit of a monostable multivibrator and explain its working principle.

8.

Explain the difference between SSI, MSI, LSI and VLSI.

- End of Question Paper -